



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Craig T. Salling et al.

Art Unit: 2815

Serial No.: 10/051,639 ✓

Examiner: Matthew C. Landau

Filed: 01/18/02 ✓

Docket: TI-32536

For: "ESD IMPROVEMENT BY A VERTICAL BIPOLAR TRANSISTOR WITH LOW BREAKDOWN VOLTAGE AND HIGH BETA"

#6/ELECTION
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Ginger Cox
Ginger Cox

02/10/2003
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ELECTION

Sir:

Responsive to the Office Action dated January 16, 2003, please enter the following:

For prosecution, Applicants elect Group I, Claims 1-8 and 19, drawn to a semiconductor device, classified in class 257, subclass 565.

Applicants acknowledge that Group II, Claims 9-18, will be withdrawn from further examination.

A prompt action on the merits is respectfully requested.

Respectfully submitted,

Gary C. Honeycutt
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